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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	10/609,311
Filing Date	June 26, 2003
Inventor	Donald L. Yates
Assignee	Micron Technology, Inc.
	1756
	Kathleen Duda
Attorney's Docket No	Ml22-2236
Title	Semiconductor Processing Patterning Methods

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Reference - - See attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98. Pursuant to Federal Register Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. No admission is made regarding whether the submitted reference is prior art.

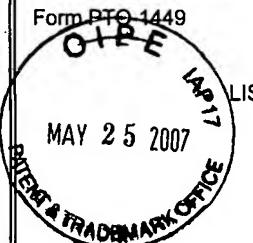
This Supplemental Information Disclosure Statement is being filed with a Response to Final Office Action to Accompany Request for Continued Examination (RCE). Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925

Citation of these references is respectfully requested.

Respectfully submitted,

Date: 5/22/2

Robertoc. Hyta Reg. No. 46,791



U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)

ATTY. DOCKET NO. MI22-2236

SERIAL NO. 10/609,311

APPLICANT: Donald L. Yates

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Examiner's Initials		Document Number	Date	Name		Class	Subclass	s Filing Date If Appropriate	
	AA	6,169,019	01/01	Takagi					
	AB	6,943,126	09/05	Narayanan et al.					
	AC	2004/0219736	11/04	Yoshitaka					
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